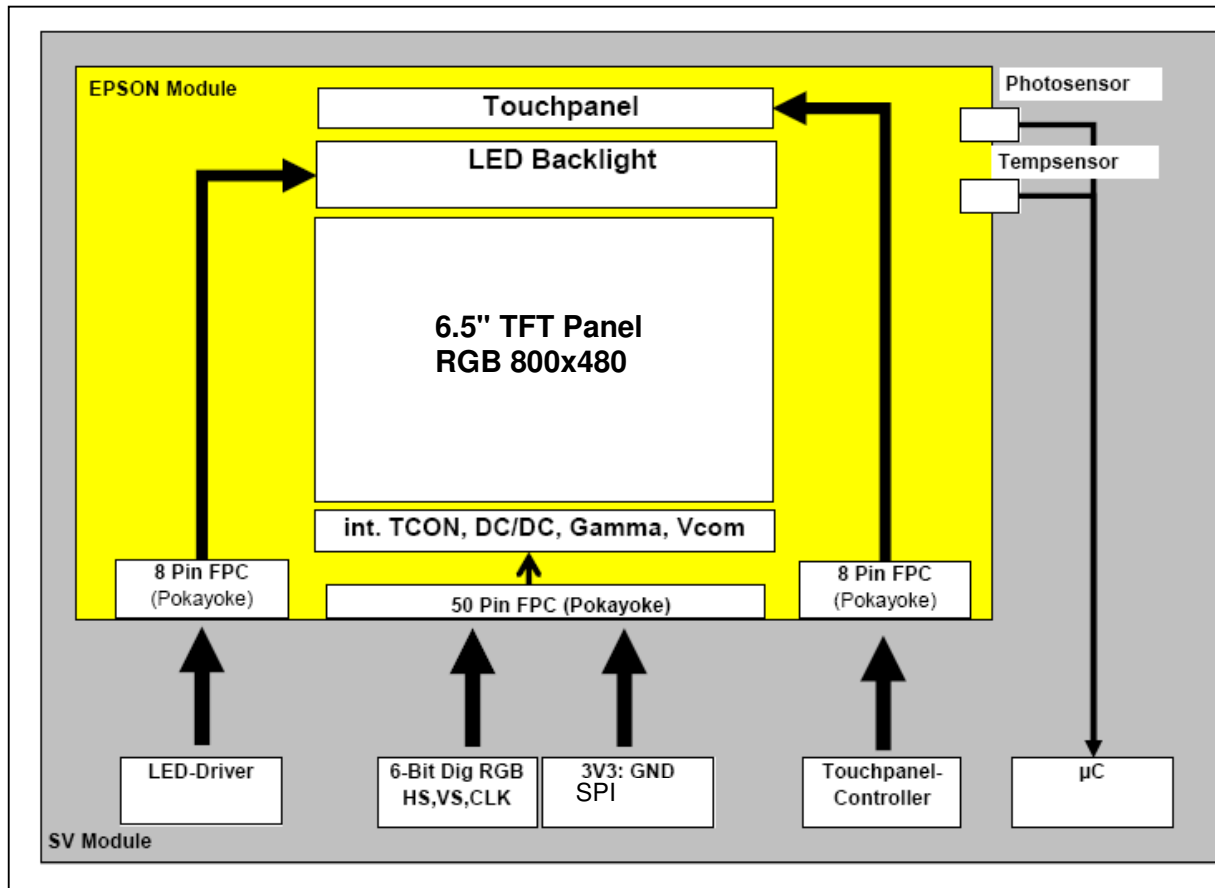


1.2 Block diagram



The shown block diagram gives an overview of the requested TFT panel (marked yellow) and its application circuit.

Backlight and display have to be packaged with housing (metal housing is required).

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4 Electrical interface (panel)

4.1 Interface


The supplier has to specify the functional description of board to board connector terminals (signal name, signal functional description, timing chart, input level threshold, ...)

4.1.1 CN1 Main Connector

The supplier has to specify the pin numbers, timing controller input signals and the number of ground pins needed in addition. The supplier has to define the pin assignment together with CONTINENTAL.

Pin No.	Symbol	Description	Remarks
1	GND_SHIELD	Ground	
2	GND_DISPLAY	Ground	
3	R(0)	Display Data (Red)	(LSB)
4	R(1)	Display Data (Red)	
5	R(2)	Display Data (Red)	
6	R(3)	Display Data (Red)	
7	R(4)	Display Data (Red)	
8	R(5)	Display Data (Red)	(MSB)
9	GND_DISPLAY	Ground	
10	GND_SHIELD	Ground	
11	GND_DISPLAY	Ground	
12	G(0)	Display Data (Green)	(LSB)
13	G(1)	Display Data (Green)	
14	G(2)	Display Data (Green)	
15	G(3)	Display Data (Green)	
16	G(4)	Display Data (Green)	
17	G(5)	Display Data (Green)	(MSB)
18	GND_DISPLAY	Ground	
19	GND_SHIELD	Ground	
20	GND_DISPLAY	Ground	
21	B(0)	Display Data (Blue)	(LSB)
22	B(1)	Display Data (Blue)	
23	B(2)	Display Data (Blue)	
24	B(3)	Display Data (Blue)	
25	B(4)	Display Data (Blue)	
26	B(5)	Display Data (Blue)	(MSB)
27	GND_DISPLAY	Ground	
28	GND_SHIELD	Ground	
29	GND_DISPLAY	Ground	

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Pin No.	Symbol	Description	Remarks
30	GND_DISPLAY	Ground	
31	PCLOCK	Clock for display data	
32	GND_DISPLAY	Ground	
33	GND_DISPLAY	Ground	
34	VSYNC	Vertical synchronous signal	
35	GND_DISPLAY	Ground	
36	GND_SHIELD	Ground	
37	GND_DISPLAY	Ground	
38	HSYNC	Horizontal synchronous signal	
39	GND_DISPLAY	Ground	
40	3V3	Power supply	
41	3V3	Power supply	
42	GND_3V3	Ground	
43	GND_3V3	Ground	
44	n_shutdown	Reset	
45	GND_SHIELD	Ground	
46	SPI (XCS)	Chip select for serial data	
47	SPI (SCL.)	Serial clock	
48	SPI (SDIN)	Serial data	
49	SPI (SDOUT)	Serial data	
50	GND_SHIELD	Ground	

4.1.2 CN2 LED Connector

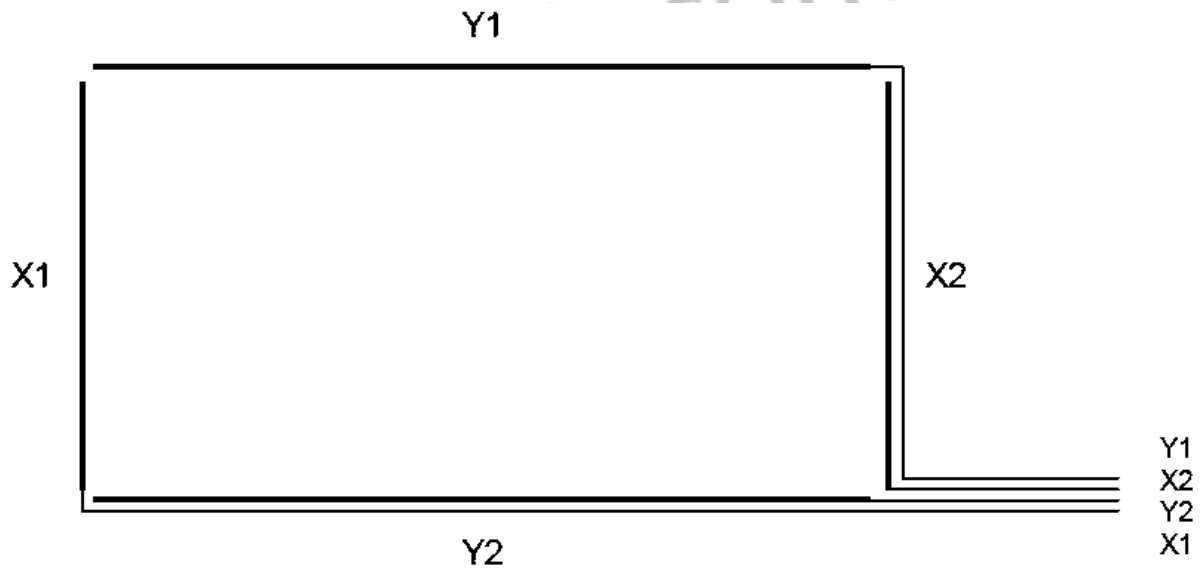
Pin No.	Symbol	Description	Remarks
1	Anode Chain 1		
2	Cathode Chain 1		
3	Anode Chain 2		
4	Cathode Chain 2		
5	Anode Chain 3		
6	Cathode Chain 3		
7	Anode Chain 4		
8	Cathode Chain 4		

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4.1.3 CN3 Touchpanel Connector

Symbol	PIN	I/O	Description	Remark
Y1	1	I/O	top wire of the touchpanel	
Y1	2	I/O	top wire of the touchpanel	
X2	3	I/O	right wire of the touchpanel	
X2	4	I/O	right wire of the touchpanel	
Y2	5	I/O	bottom wire of the touchpanel	
Y2	6	I/O	bottom wire of the touchpanel	
X1	7	I/O	left wire of the touchpanel	
X1	8	I/O	left wire of the touchpanel	



4.2 Display driver

4.2.1 Block diagram

The supplier has to provide a block diagram to illustrate the electronic functionality.

4.2.2 Characteristics

The supplier has to provide specification and information about the electronic devices integrated on glass.

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